



# Parametric Investigation of Near End and Far End Crosstalks in Printed Circuit Board Lands

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## Abstract

Multi-conductor transmission line and interconnect carry signals with wide range of frequencies from sending end to receiving end. The signal in one transmission line may be interrupted by the unwanted contributions from the neighboring line conductors. If data speed increases, high frequency effects occur and the signals suffer from difficulties such as ringing, crosstalk, reflections, and ground bounce that seriously hamper the quality of the received signal. In order to estimate the signal quality, signal integrity analysis is needed. In this paper, an attempt has been made to investigate the sensitivity of the near and far end crosstalk on the parameters such as physical geometry of the conductors, electrical property of the substrate and the rise and fall time of excitation signal. The method of moments (MOM) is used to calculate the line parameters for different geometries. The simulation studies are carried out in TNT. The time domain and frequency domain analyses are performed using transmission line model of PSpice. Moreover, a model is developed and tested in the laboratory. It is observed that the coupling inductance and capacitance vary with the variation of physical geometry and the substrate parameter.

**Keywords:** Multi-conductor transmission line (MTL), method of moments, crosstalk, near end and far end

## 1. Introduction

In a multi-conductor transmission line (MTL) system, the coupling capacitance, inductance among line conductors cause crosstalk. For example in a three line system, the voltage on line 1 will contain the unwanted contributions from line 2 and 3 adjacent to it. The voltage on line 1 in a three conductor system can be expressed as (1).

$$v_1 = L_{11} \frac{di_1}{dt} + L_{12} \frac{di_2}{dt} + L_{13} \frac{di_3}{dt} \quad (1)$$

where  $L_{11}$  is the self inductance of line 1,  $L_{12}$  and  $L_{13}$  are the coupling inductances on line 1 due to line 2 and 3 respectively. A similar equation for the current can be obtained using self and mutual capacitances. Crosstalk contaminates desired signal and imposes a limiting factor for high speed communication. The crosstalk degrades the signal speed and integrity significantly [1]-[12]. The transmission line which carries the crosstalk is called a victim line. The aggressor line is terminated to prevent reflections. The victim line is terminated at both ends with no other loads. The cross talk will propagate in both forward and backward directions. The voltage response measured on the victim line near the original source is called near end crosstalk. The voltage response measured at the other end is called far end crosstalk. Each type has very different characteristics [1], [2]. Multi-conductor signal lines are using in many high speed analog and digital electronic printed circuit boards (PCBs), and multi-conductor telephone cables. In addition, twisted pair cables have distributed self and mutual series inductance and parallel capacitances. The transmission line which has the source signal propagated is called the aggressor line [13-21]. In this paper, a parameter dependence of the near and far end crosstalk (NEXT and FEXT) among the PCB lands has been investigated both in time and frequency domains. Fig. 1. depicts the necessary steps which are followed in this analysis. In order to validate the calculated results experimental investigations are underway using a time domain reflectometer (TDR).

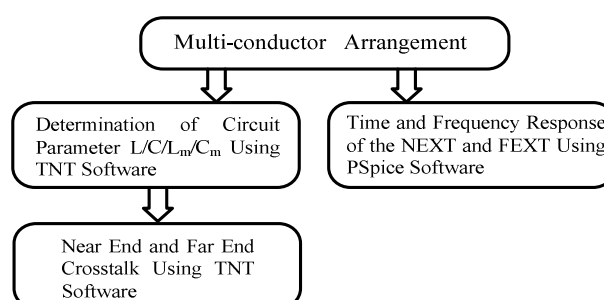


Figure 1. Block diagram for NEXT and FEXT investigation process

## 2. MTL Equations

The multiconductor transmission line equation can be derived from the distributed parameters model [3], [5]. The distributed parameter model of an MTL system is shown in Fig. 2. The coupled differential equations can easily be written using Kirchhoff's voltage and current laws as (2) and (3).

$$\frac{\partial}{\partial z} V(z, t) = -L \frac{\partial}{\partial t} I(z, t) \quad (2)$$

$$\frac{\partial}{\partial z} I(z, t) = -C \frac{\partial}{\partial t} V(z, t) \quad (3)$$

where the entries in the voltage and current vectors  $V(z, t)$  and  $I(z, t)$  are given as

$$V(z, t) = \begin{bmatrix} V_G(z, t) \\ V_R(z, t) \end{bmatrix} \quad (4)$$

$$I(z, t) = \begin{bmatrix} I_G(z, t) \\ I_R(z, t) \end{bmatrix} \quad (5)$$

$$L = \begin{bmatrix} l_G & l_m \\ l_m & l_R \end{bmatrix} \quad (6)$$

$$C = \begin{bmatrix} C_G + C_m & -C_m \\ -C_m & C_R + C_m \end{bmatrix} \quad (7)$$

where  $l_G, l_R$  and  $C_G, C_R$  are per unit length self inductance and self capacitance of aggressor and victim lines respectively and,  $l_m, C_m$  are mutual inductance and mutual capacitance between them that mainly responsible for crosstalk hence signal integrity [18]. Analytical and numerical methods are generally used to solve these equations for predicting the crosstalk. Determination of the line parameters as shown in (6) and (7) is the key step for solving the coupled differential equations both in time and frequency domains. Time domain numerical techniques such as finite difference time domain (FDTD) method is one of the widely used method though it has stability problem and huge computational time. The circuit model of the transmission can be designed in the SPICE/PSPICE using calculated parameters. This model can also be used to determine the time and frequency domain response of the NEXT and FEXT in the victim line [3], [5]. In this paper, the line parameters are calculated using a non commercially available numerical MTL program TNT [9]. The results are used to draw the SPICE/PSPICE model or OrCAD 10 model. PSPICE is used to simulate the time and frequency domain response for different parameters variation.

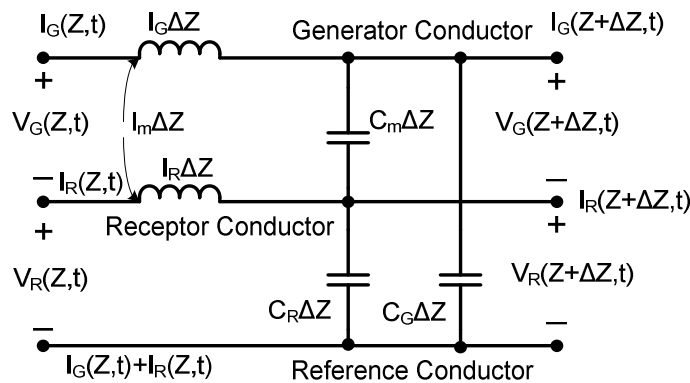


Figure 2. The per unit length distributed parameter model of a three conductors transmission line [1]

## 3. Determination of Per Unit Length Parameters

The basic structure of a three conductor PCB system is depicted in Fig. 3. The geometry of the structure is drawn in the graphical window of TNT program, and the size and parameter of the substrate is specified. The value of the per unit length parameters are calculated from the designed structure using TNT program. Along with

parameters it can calculate the near and far end crosstalk in dB. Trapezoidal pulse is used as the excitation signal in aggressor line for matched resistive termination [13].

The leftmost line of Fig. 3. is designated as the reference conductors. The typical values of dimensions used in this calculation are  $w = 15$  mils,  $s = 47$  mils, and  $h = 47$  mils. The dielectric constant of  $\epsilon_r = 4.7$  (glass epoxy) is utilized and each land are divided into 30 subsections. The computed per unit length inductance and capacitance are shown in Table 1(a) and Table 1(b). The per unit length capacitance vary with  $\epsilon_r$ , but the per unit length inductance remain invariant.

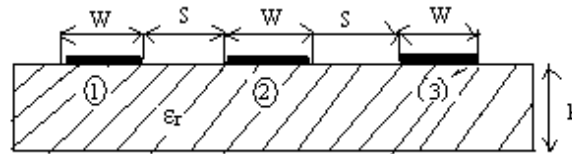


Figure 3. The layout of three lines on a PCB used for calculating per unit length capacitances

Table 1. MTL line parameters for different values of  $\epsilon_r$   
(a) Capacitance per unit length

$\epsilon_r$	$C_{11}$ (pF/m)	$C_{12}/C_{21}$ (pF/m)	$C_{22}$ (pF/m)
2.94	39.694150	- 8.8280570	39.707182
3.02	40.507230	- 8.9294670	40.520639
3.90	49.432774	- 10.450446	49.450553
4.26	53.076817	- 10.454460	53.096310
4.70	57.526567	- 11.806769	57.547498
4.80	8.537439	- 11.881779	58.559116
6.15	72.167994	- 14.270159	72.195527

(b) Inductance per unit length

$\epsilon_r$	$L_{11}$ ( $\mu$ H/m)	$L_{12}/L_{21}$ ( $\mu$ H/m)	$L_{22}$ ( $\mu$ H/m)
2.94	0.61066953	0.1705723	0.61066913
3.02	0.61066953	0.1705723	0.61066913
3. 0	0.61066953	0.1705723	0.61066913
4.26	0.61066953	0.1705723	0.61066913
4.70	0.61066953	0.1705723	0.61066913
4.80	0.61066953	0.1705723	0.61066913
6.15	0.61066953	0.1705723	0.61066913

#### 4. Calculation of NEXT and FEXT by TNT in dB

The relative magnitudes of the NEXT and FEXT are calculated for the aforementioned configuration (Fig. 3.) and the results are shown in Fig. 4(a), Fig. 4(b), Fig. 4(c) and Fig. 4(d). It is observed from the results that the magnitude of NEXT and FEXT dependent on  $\epsilon_r$ . An increase of  $\epsilon_r$  increases the value of the coupling capacitance that is consequently boost crosstalk.

#### 5. Time Domain and Frequency Domain Analysis of NEXT and FEXT

The equations (2) and (3) are coupled equations. They could be uncoupled to modal equations using similarity transformation. This technique has been developed in [2]. The complete SPICE model for the PCB structure is shown in Fig. 5 (a) and corresponding node numbering for the SPICE subcircuit model is also shown in Fig. 5 (b). The time domain analysis of NEXT and FEXT are made considering CW sine and periodic pulse signal. Signal is applied to the near end of the multiple lines and signal integrity is monitored by mixing of signals due to crosstalk. Fig. 6 (a) and Fig.(b) depict the NEXT and FEXT, respectively, for a 10 V (p-p) and 0.5 GHz sine signal applied in conductor 1 of PCB. The line parameters as shown in table 1 and 2, and  $R_S = R_L = Z_{C1} = 195.6 \Omega$ ,  $R_{NE1} = R_{FE1} = Z_{C2} = 147.5 \Omega$ ,  $R_{NE2} = R_{FE2} = Z_{C3} = 94.41 \Omega$ ,  $R_{NE3} = R_{FE3} = Z_{C4} = 204.33 \Omega$  are used in the SPICE model.

In order to investigate the signal contamination, two lines are excited by two signals, one 10 V (p-p), 0.5 GHz sine signal applied in land 1 and other 10 V, 10 ns time period pulse signal applied in land 2 and the near and far end signals in the lines are monitored. The results are shown in Fig. 7(a) for far end. It can easily be visualized

from the figures that the original signal on both lines are contaminated by the crosstalk contribution from others. In order to monitor the signal integrity experimentally, a sinusoidal signal is applied between the ground and first conductor and a square wave is applied between the second conductor and ground. It has been observed that the second conductor signal is contaminated by the signal in the first conductor due to crosstalk. The oscillogram of the experiment is shown in Fig. 7 (b) and that agreed with the simulated result presented in Fig. 7 (a)

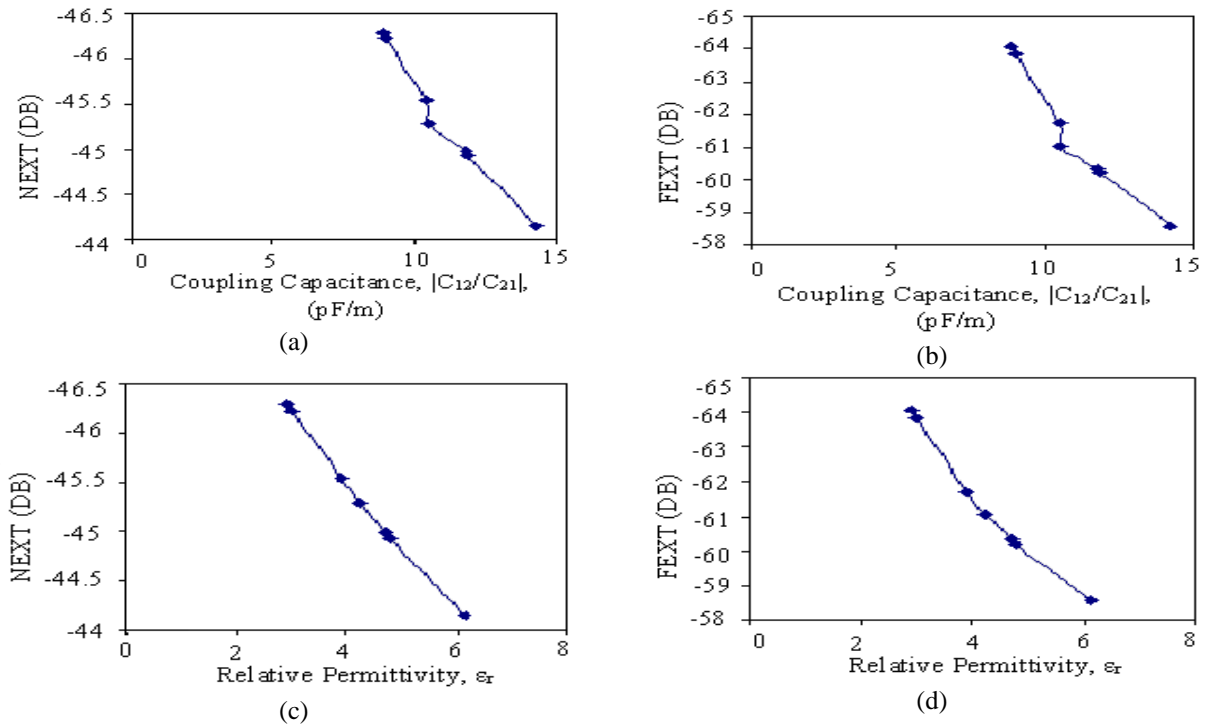


Figure 4. (a) NEXT versus coupling capacitance, (b) FEXT versus coupling capacitance, (c) NEXT versus  $\epsilon_r$  and (d) FEXT versus  $\epsilon_r$

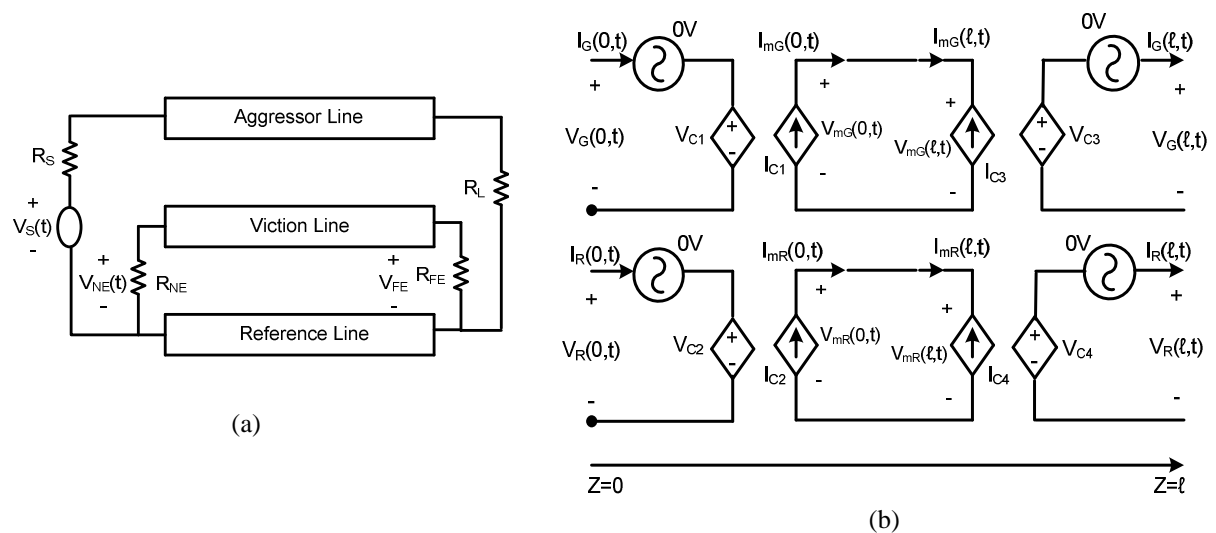
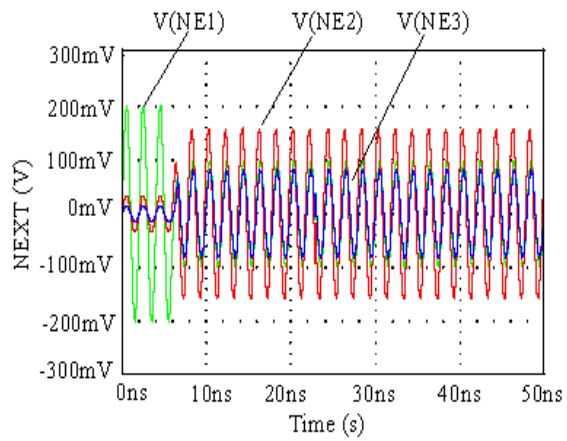
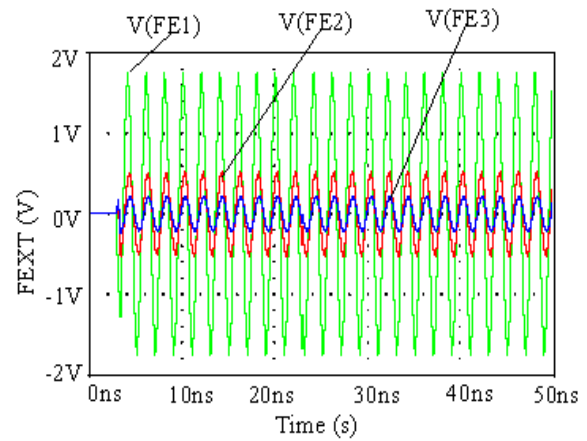


Figure 5. (a) Three conductor line model (b) The complete SPICE model [2], [8]

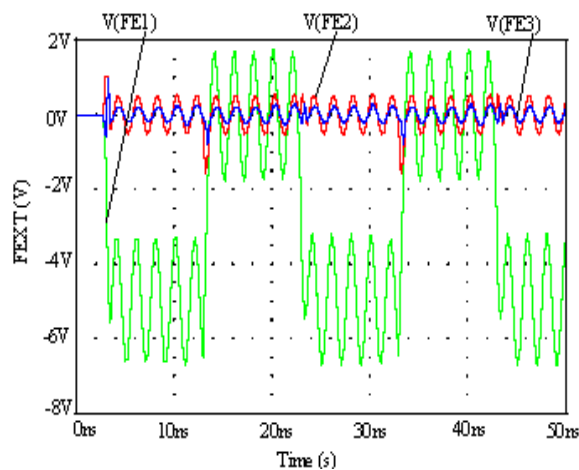


(a)

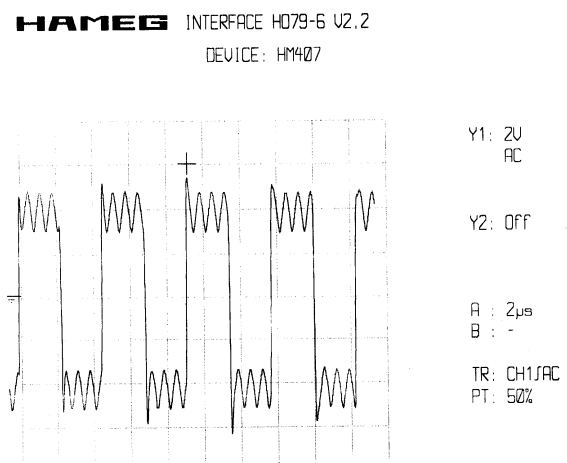


(b)

Figure 6 (a) NEXT and (b) FEXT for sinusoidal signal in the first conductor for  $\epsilon_r = 3.9$ ,  $w = 15$  mils,  $d = 45$  mils,  $h = 47$  mils.

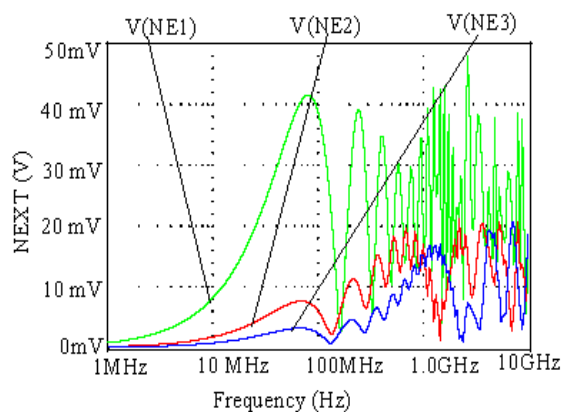


(a)

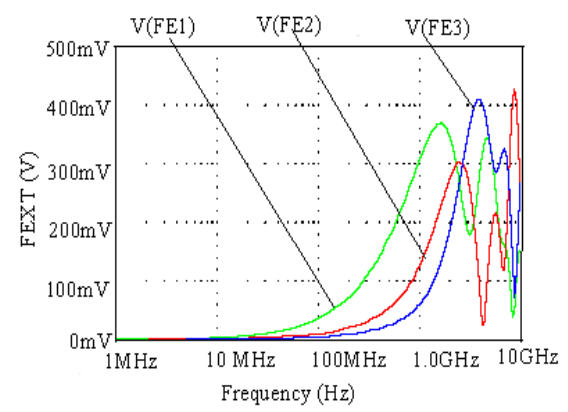


(b)

Figure 7. Signal contamination due to contributions from signals from neighboring lines. (a) Far end signals and (b) Measured signal at the far end ( $h = 62$  mils,  $\epsilon_r = 3.9$ ,  $w = 15$  mils,  $d = 45$  mils).



(a)



(b)

Figure 8. (a) NEXT and (b) FEXT in frequency domain. The signal is applied in the first line.

The sine signal in the first line is coupled with the pulse train in the second line and the signal integrity is lost eventually. As the far end terminations on the lines are not perfectly matched, the far end signals are contaminated greatly due to the reflection. The frequency domain analysis is performed using sinusoidal source 1V (p-p). The source frequency is swepted from 1 MHz to 10 GHz. The results are shown in Fig. 8 (a), (b). It can be seen from the figure that magnitude of NEXT and FEXT increases with frequency and at high frequency region some undulation appears and this will limit the speed of signal transmission over the MTL structure.

## 6. Conclusion

This paper demonstrates and measures near end and far end crosstalk and signal integrity in MTL structure etched on PCB. It has been observed that the magnitude of NEXT and FEXT are very sensitive with the size of conductors and substrate parameters. The rise and fall time of excitation signal also affect the NEXT and FEXT. For a fixed set of parameters the magnitude of the crosstalk increases with increasing frequency. This type of analysis is particularly important to design PCB layout and interconnects that handle high speed data.

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## Bibliography



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